



CYPR-CD01209M

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nemecek

Serial No.: 09/998,859

Filing Date: November 15, 2001

FOR: A SYSTEM AND A METHOD FOR CHECKING
LOCK STEP CONSISTENCY BETWEEN AN
IN CIRCUIT EMULATION AND A
MICROCONTROLLER WHILE DEBUGGING
PROCESS IS IN PROGRESS

Examiner: Thompson, A. M.

Art Unit: 2825

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Office Action mailed November 19, 2003, the following remarks and supporting declaration to the above captioned patent application are respectfully submitted. The Applicants have filed an Information Disclosure Statement and a Request for Continued Examination herewith. Reconsideration of the above captioned patent application is respectfully requested.

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